



REQUEST FOR RECONSIDERATION

Application #	10/828,983
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First Inventor	RAPAICH, MARK
Art Unit	2114
Examiner	SCHELL, JOSEPH O.
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In response to the Office Action dated April 19, 2007, reconsideration of the rejection of the claims is respectfully requested.

Claims 1 – 7, 9 – 15, 17 – 21 and 23 – 28 have been rejected under 35 U.S.C. 102(b) as being anticipated by Lin (U.S. Patent No. 6,862,695). This rejection is respectfully traversed.

Claims 1, 11, 19 and 26 are the independent claims of the application.

Claim 1 recites an apparatus for detecting and indicating faults on a computer motherboard comprising, *inter alia*, a visual indicator coupled to and controlled by a microprocessor for providing a visual indication when a fault on the computer motherboard is detected during execution of diagnostic instructions by the microprocessor.

It is suggested in the Office Action that Lin discloses an apparatus having such a visual indicator coupled to and controlled by a microprocessor for providing a visual indication when a fault on a computer motherboard is detected during execution of diagnostic instructions by the microprocessor "as shown in Figure 5a steps 504 and 505."

Steps 504 and 505 of Figure 5a of the Lin reference are "Failure Encountered In RAM's Test Procedure?" and, if yes, "Blink LED ON And OFF At a First Frequency," respectively. The Lin reference also discloses that the LED is connected to an I/O port of a bridge chip. More particularly, Lin provides that:

"If the model name and the specification information of the
RAM 32 can not be correctly obtained, the BIOS program